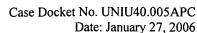




TENT RECEIPT





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)

Masahiko NAKAMORI, et al.

App. No.

10/536,621 ·

Filed

May 26, 2005

For

POLISHING PAD AND METHOD OF

PRODUCING SEMICONDUCTOR DEVICE

Art Unit

: 1763

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313, on

> January 27, 2006 (Date)

Katsuhiro Arai, Reg. No. 43,315

U.S. PATENT AND TRADEMARK OFFICE **COMMISSIONER FOR PATENTS Initial Patent Examination's Filing Receipt Corrections** P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Applicants noted error in the title of the invention in the Official Filing Receipt for the aboveidentified patent application. The correct title is --POLISHING PAD AND METHOD OF PRODUCING SEMICONDUCTOR DEVICE--, not "POLISHING PAD AND METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE". Please make this correction in the Patent and Trademark Office and issue a corrected Official Filing Receipt. As this error appears to have been made at the U.S. Patent and Trademark Office, we believe no fees to be required. However, should any fees be necessary for this request, please charge them to our deposit Account No. 11-1410.

Enclosed in support of this Request are the following:

- (X) A Copy of the Official Filing Receipt.
- (X) A Copy of the Declaration.
- (X) Return prepaid postcard.

Katsuhiro Arai

Registration No. 43,315

Attorney of Record

Customer No. 20,995

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2332044 012706

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Page 1 of 3 UNIU40.005APC DFA KON

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CONFIRMATION NO. 9275

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20995 KNOBBE MARTENS OLSON & BEAR LLP 2040 MAIN STREET FOURTEENTH FLOOR IRVINE, CA 92614

FILING OR 371

(c) DATE

05/26/2005

ART UNIT

1763

FILING RECEIPT

OC000000017283631

Date Mailed: 10/21/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

UNIU40.005APC

Applicant(s)

APPL NO.

10/536,621

Masahiko Nakamori, Shiga, JAPAN; Tetsuo Shimomura, Shiga, JAPAN; Takatoshi Yamada, Shiga, JAPAN; Kazuyuki Ogawa, Osaka, JAPAN; Atsushi Kazuno, Osaka, JAPAN; Kimihiro Watanabe, Osaka, JAPAN;

Power of Attorney: The patent practitioners associated with Customer Number 20995.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/15128 11/27/2003

Foreign Applications

JAPAN 2002-343199 11/27/2002 JAPAN 2003-000331 01/06/2003 JAPAN 2003-029477 02/06/2003 JAPAN 2003-064653 03/11/2003

Projected Publication Date: 01/26/2006

Non-Publication Request: No

Early Publication Request: No

Title

Polishing pad and method for manufacturing semiconductor device

Preliminary Class

156

4 Please correct to -- PRODUCING --.

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

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Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at http://www.uspto.gov/web/offices/pac/doc/general/index.html.

For information on preventing theft of your intellectual property (patents, trademarks and copyrights), you may wish to consult the U.S. Government website, http://www.stopfakes.gov. Part of a Department of Commerce initiative, this website includes self-help "toolkits" giving innovators guidance on how to protect intellectual property in specific countries such as China, Korea and Mexico. For questions regarding patent enforcement issues, applicants may call the U.S. Government hotline at 1-866-999-HALT (1-866-999-4158).

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DECLARATION - USA PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled POLISHING PAD AND METHOD OF PRODUCING SEMICONDUCTOR DEVICE; PCT Application No. PCT/JP2003/015128 filed in the Japanese Receiving Office on November 27, 2003; the documentation for entry into the U.S. national phase of which is attached hereto;

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above;

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56;

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

PRIOR FOREIGN APPLI	CATION(S)		<u>Priority</u> <u>Claimed</u>
No.: 2002-343199	Country: Japan	Date Filed: 27/11/02	Yes
No.: 2003-000331	Country: Japan	Date Filed: 06/01/03	Yes
No.: 2003-029477	Country: Japan	Date Filed: 06/02/03	Yes
No.: 2003-064653	Country: Japan	Date Filed: 11/03/03	Yes

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful, false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor: Masahiko NAKAMORI
Inventor's signature <u>Masahiko Nakamori</u>
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Full name of second inventor: Tetsuo SHIMOMURA
Inventor's signature Ictorio Strmomura
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